

Receiver Architectures for 5G: Current Status and Future Prospects

CMOS Analog IC Design for 5G and Beyond pp 79-88 | Cite as

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Chapter First Online: 08 February 2021

• 122 Downloads

Part of the Lecture Notes in Electrical Engineering book series (LNEE, volume 719)

Abstract

In this chapter, the recent progress in receiver architecture and various aspects of the available receiver architectures have been discussed. Besides, an overview of the systematic classification of architecture has been analyzed. Documentation of new possibilities and system-level trade has been closely inspected. Certainly, there is a requirement of low-power, flexible, and high-performance receiver architecture for the successful implementation of the 5G network. Different works in this regard have been considered as examples for discussing the status and prospects of architectures with respect to 5G future. Various architectures considered in this chapter can be very valuable to design 5G network in future and will expose the research community with new possibilities to explore for further improvements.

Keywords

Wireless communication 5G Receiver architectures RF mixer architecture Baseband TIA I/Q generation This is a preview of subscription content, <u>log in</u> to check access.

Notes

Acknowledgements

Amitesh Kumar is thankful to Department of Electrical Engineering, National Institute of Technology, Patna for providing resources to write this book chapter. Amitesh Kumar is thankful to Council of Scientific and Industrial Research to provide research fellowship to carry out research during his stay at Indian Institute of Technology, Indore.

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About this chapter

Cite this chapter as:

Kumar A. et al. (2021) Receiver Architectures for 5G: Current Status and Future Prospects. In: Singh S., Arya R., Singh M.P., Iyer B. (eds) CMOS Analog IC Design for 5G and Beyond. Lecture Notes in Electrical Engineering, vol 719. Springer, Singapore. https://doi.org/10.1007/978-981-15-9865-4_5

- First Online 08 February 2021
- DOI https://doi.org/10.1007/978-981-15-9865-4_5
- Publisher Name Springer, Singapore
- Print ISBN 978-981-15-9864-7

- Online ISBN 978-981-15-9865-4
- eBook Packages Engineering Engineering (Ro)
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